

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE



Membership | Publications/Services | Standards | Conferences | Careers/Jobs

Welcome
United States Patent and Trademark
Office

IEEE Xplore®
1 Million Documents
1 Million Users

[Help](#) [FAQ](#) [Terms](#) [IEEE Peer
Review](#)

[Quick Links](#)[» Search Results](#)[Welcome to IEEE Xplore](#)

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

[Index of Contents](#)

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

[Search](#)

- ☐ By Author
- ☐ Basic
- ☐ Advanced
- ☐ CrossRef

[Membership Services](#)

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

[IEEE Journals](#)

- ☐ Access the IEEE Electronic File Cabinet

Your search matched **5** of **1134355** documents.A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.**Refine This Search:**

You may refine your search by editing the current search expression or entering a new one in the text box.

☐ Check to search within this result set**Results Key:****JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard**1 Design of multidimensional finite-wordlength FIR and IIR filters by simulated annealing***Radecki, J.; Konrad, J.; Dubois, E.;*

Circuits and Systems II: Analog and Digital Signal Processing, IEEE

Transactions on [see also Circuits and Systems II: Express Briefs, IEEE Transactions on], Volume: 42, Issue: 6, June 1995

Pages:424 - 431

[\[Abstract\]](#) [\[PDF Full-Text \(780 KB\)\]](#) **IEEE JNL****2 Three-dimensional pre- and post-filtering for PAL TV signals***Teichner, D.;*

Consumer Electronics, IEEE Transactions on, Volume: 34, Issue: 1, Feb. 1988

Pages:205 - 227

[\[Abstract\]](#) [\[PDF Full-Text \(1760 KB\)\]](#) **IEEE JNL****3 FPGA implementation of Kalman low-pass filter for applications in sigma-delta (/spl Sigma/-/spl Delta/) demodulation***Charoensak, C.; Abeysekera, S.S.;*

Signal Processing Systems, 2003. SIPS 2003. IEEE Workshop on, 27-29 Aug. 2003

Pages:219 - 223

[\[Abstract\]](#) [\[PDF Full-Text \(367 KB\)\]](#) **IEEE CNF****4 Accurate time delay estimation based on SINC filtering***Wen Ma; Jianguo Huang;*

Signal Processing, 2002 6th International Conference on, Volume: 2, 26-30 Aug. 2002

Pages:1621 - 1624 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(295 KB\)\]](#) **IEEE CNF****5 Optimum sigma-delta ($\Sigma\Delta$) de-modulator filter Implementation via FPGA***Abeysekera, S.S.; Charoensak, C.;*

ASIC/SOC Conference, 2001. Proceedings. 14th Annual IEEE International, 12-15 Sept. 2001

Pages:281 - 285

[\[Abstract\]](#) [\[PDF Full-Text \(408 KB\)\]](#) **IEEE CNF**

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved